





电性参数

Electrical Parameters

Ta=25 Absolute Maximum Rating(ta=25)

Supply Voltage	VDD	3.5-5.5	V
Logic input voltage	V_{IH}		

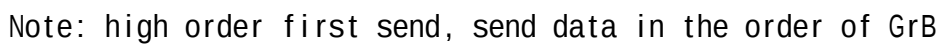
Ta=25 Optical-electrical parameter(ta=25)



O



24bit 24bit data structure



Timing diagram for the RST pin showing three cases:

- 0码**: The RST pin signal is high for a duration T_0 and low for a duration T_1 . The signal transitions from high to low at T_{in0h} and from low to high at T_{in1h} .
- 1码**: The RST pin signal is low for a duration T_0 and high for a duration T_1 . The signal transitions from low to high at T_{in0h} and from high to low at T_{in1h} .
- RST码**: The RST pin signal is high for a duration T_{reset} and low for a duration T_{reset} . The signal transitions from low to high at T_{in0h} and from high to low at T_{in1h} .





