

WS2

1.F5

The F55x iAN for Onig@2019-06-08 PM 5:54

dips*ng

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可靠性实验

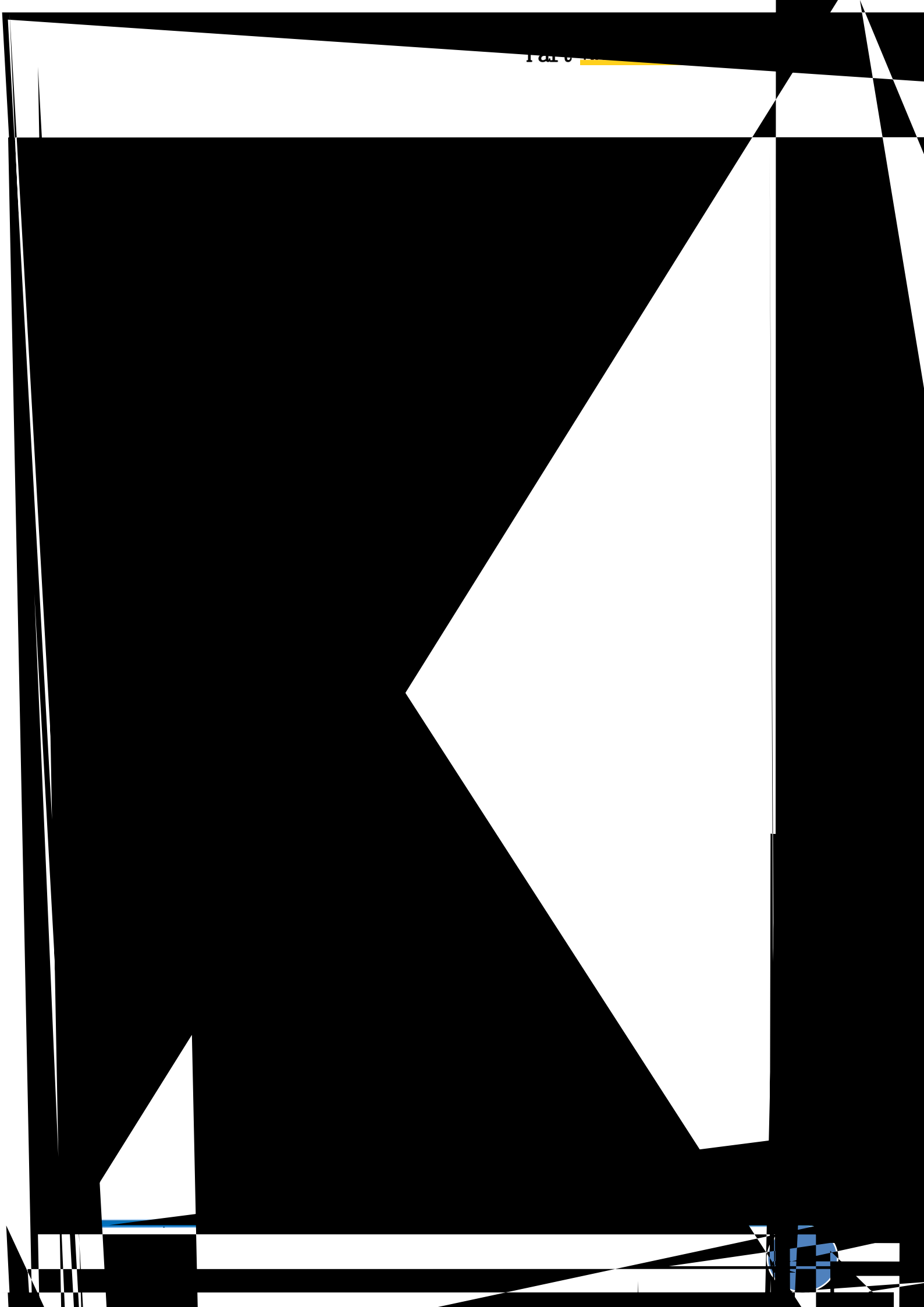
外形尺寸

包装

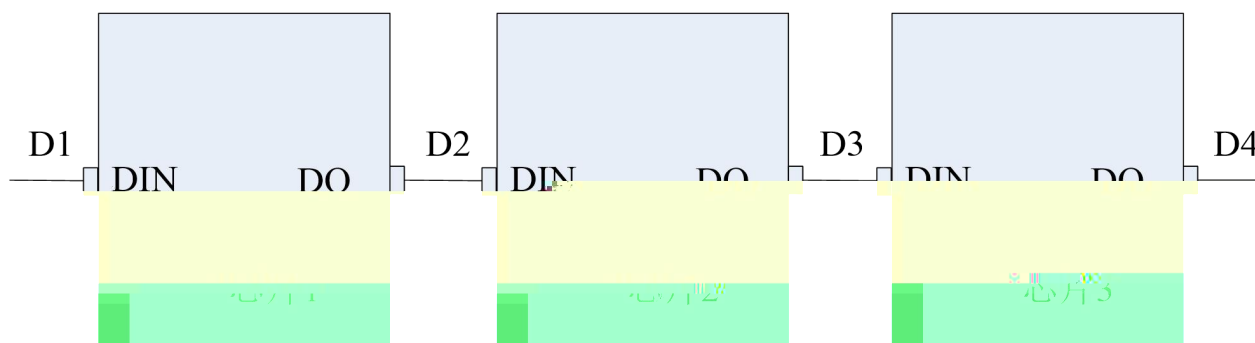
使用注意事项



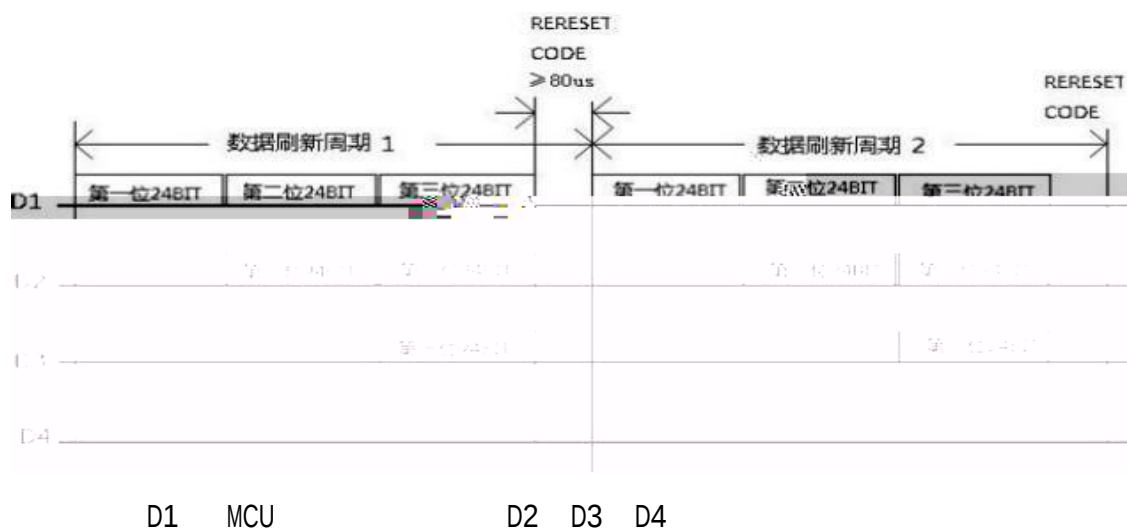
Puñ-fH Po VV



:Chip cascade method:



Date transmission mode



Note: D1 refers to the data sent by MCU, and D2, D3 and D4 refer to the data automatically shaped and forwarded by cascade circuit

24bit 24bit data structure

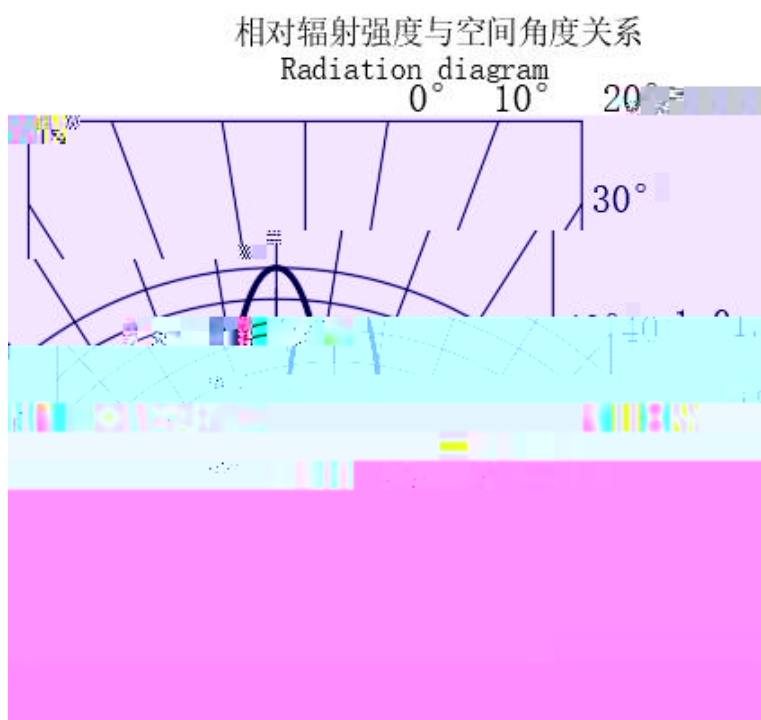
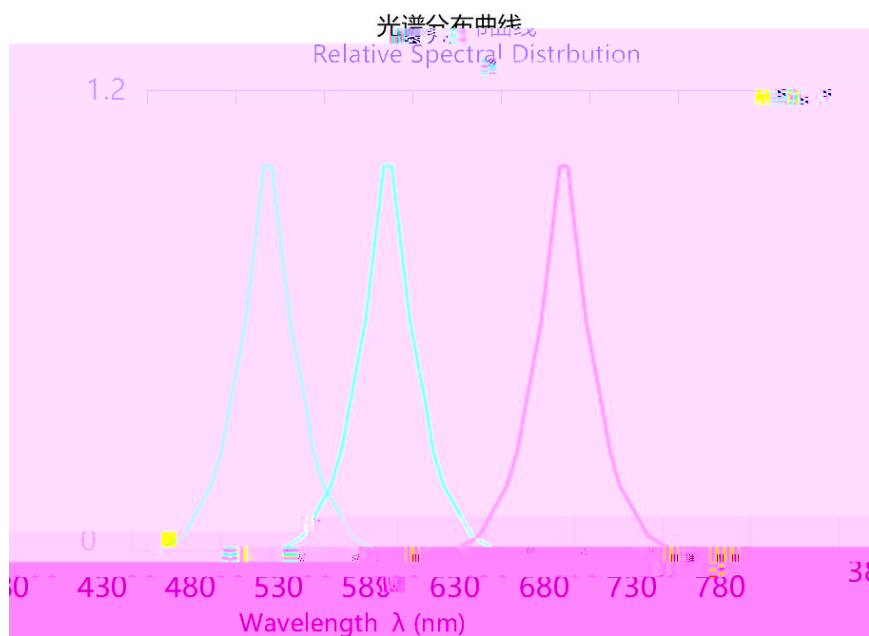


Note: high order first send, send data in the order of GrB



典型特性曲线

Typical Characteristics Curves







包装

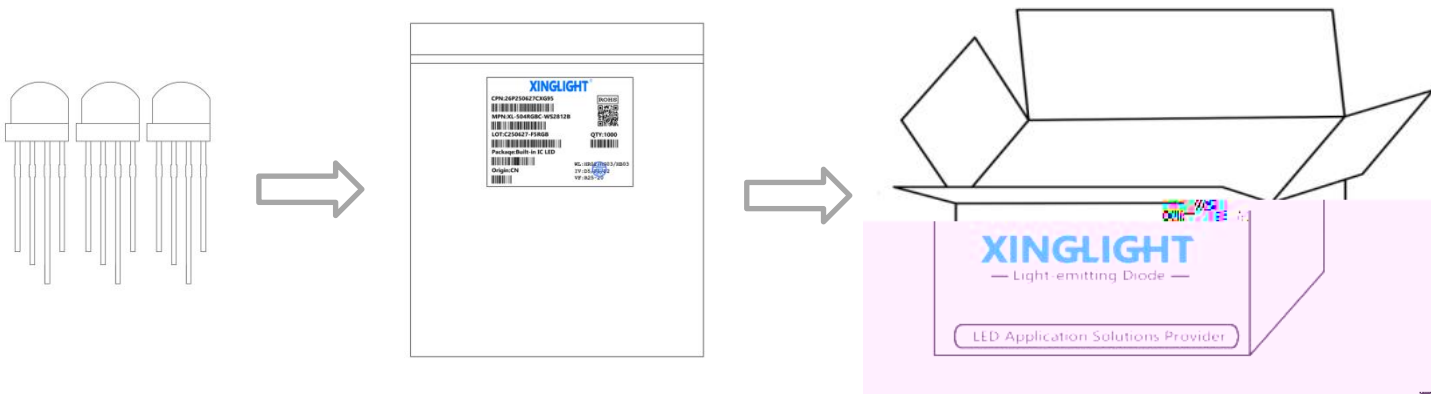
Packaging

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In Bags

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Moisture Proof and Anti-Electrostatic Foil Bag



◇ 标签说明 Label Expansion

CPN: 批号/档位

LOT: 日期/封装颜色

MR: 型号

VE: 电压代码







